MINISTRY OF COMMUNICATIONS AND INFORMATION TECHNOLOGY

(\textit{Department of Electronics and Information Technology})

\textbf{NOTIFICATION}

New Delhi, the 14th September, 2012

G.S.R. 689(E).—In exercise of the powers conferred by the sub-section (2) of Section 96 of the Semiconductor Integrated Circuits Layout-Design Act, 2000 (37 of 2000), the Central Government hereby makes the following rules to amend the Semiconductor Integrated Circuits Layout-Design Rules, 2001 namely:—

1. (1) These rules may be called the Semiconductor Integrated Circuits Layout-Design (Amendment) Rules, 2012.

1. They shall come into force on the date of their publication.

2. In the Semiconductor Integrated Circuits Layout-Design Rules, 2001 in rule 10,—

(a) in sub-rule (3), the words “by the bank draft”, the words “by a bank draft payable to the Pay & Accounts Officer, Department of Electronics and Information Technology” shall be substituted;

(b) in sub-rule (4), for the words “made payable to Registrar”, the words “made payable to the Pay & Accounts Officer, Department of Electronics and Information Technology” shall be substituted.

[\textbf{F. No.2(1)/2011-SICLDR}]

Dr. ANITA BHATNAGAR JAIN, \textit{Jt. Secy.}