MINISTRY OF COMMUNICATIONS AND
INFORMATION TECHNOLOGY

(Department of Information Technology)

NOTIFICATION

New Delhi, the 11th April, 2011

S.O. 746(E).—In exercise of the powers conferred by Sub-section (3) of Section 1 of the Semiconductor Integrated Circuits Layout-Design Act, 2000 (37 of 2000), the Central Government hereby appoints the 1st day of May, 2011 as the date on which the provisions of Sections 1 (1 & 2), 2, 3, 4, 6 to 31, 34, 35, 36 to 92, 95 and 96 of the said Act shall come into force.

[F. No.12(31)/99-JPC(Vol.V)]

N. RAVI SHANKER, Jt. Secy.