## FORM LD-5

## REQUEST FOR STATEMENT OF GROUNDS OF DECISION

[Rule 27(1)]

	In the matter of the Registrar is here				
the	theday of20_ after hearing	on the ——da	ry of ———	–20— and the mate	rials used by
him	him in arriving at the decision.	X1 2		, 19 m	
Dat	Dated this — day of — 20—.			ø	
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To				and the second	
	The Registrar of the Semiconductor Integrated	Circuits Layout-	Design, Office	of the Semiconducto	or Integrated
Cir	Circuits Layout-Design Registry at3	f		2 ** Y 1 1 Y 1	
	1. Insert particulars identifying the application.				
2.	<ol><li>Signature of the applicant or his agent (legal practitioner or to of the applicant - see section 84).</li></ol>	registered layout-de	sign agent or perso	n in the sole and regula	ar employment
3.	3. State the name of the place of the appropriate office of the	Semiconductor Inte	egrated Circuits La	yout-Design Registry.	See rule 4.